Fabian Mihailowitsch November 26, 2010



Who?

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 - Penetration Tests
 - Source Code Reviews

Contact

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What?

- Hardware Keylogger
 - ▶ PS/2
 - ► USB



Hardware Keyloggers are undetectable by Software

"Visual inspection is the primary means of detecting hardware keyloggers, since there are no known methods of detecting them through software. ", en.wikipedia.org, 05.11.10

Talk: Detection of Hardware Keyloggers with Software ;)

Why?

Less research on this topic

- Few information
- No practical way to detect HKL

Because HKL are a threat

- > 2005 (GB): Sumitomo Bank
 - Attackers tried to steal 423 million USD
 - Multiple HKL were installed
- How about your company?

Solution to identify HKL in large enterprises

- Visual inspection is impractical
- Only possible via software

Hardware Keylogger

- Hardware Keylogger
 - ► USB
 - ▶ PS/2
 - Keyboard Module
 - Mini- / PCI card
- Installed between PC and Keyboard
 - Records key strokes
- Captured data are retrieved
 - Software
 - Keyboard
 - Ghost typing
 - Flash drive
 - Wi-Fi-Access
 - Email
 - TCP connect
 - Bluetooth







Hardware Keylogger

Features

- Up to 2 GB flash memory
- Encryption
- Password protection
- Timestamping
- Time use charts
- Search functions
- Upgradeable firmware

Pricing

- ▶ PS/2: 32.00 USD
- ▶ USB: 58.00 USD

Hardware Keylogger – The companies

- Big ones
 - ▶ KeyDemon, KeeLog, ... (PL)
 - KeyCarbon (US)
- Most companies rebrand KeyDemon
 - KeyCobra
 - KeyLlama (once own products)
 - ► ...
- Also "famous" (older products)
 - KEYKatcher (US)
 - KeyGhost (NZ)
 - KeyShark (DE)
- The others
 - WirelessKeylogger (UK)
 - Exotic Stuff (mostly CN)
 - Some Open Source Keylogger

- Keyboard
 - Wire matrix
 - Microcontroller
 - Sends scancode (make/break)

PC

- Keyboard Controller (KBC)
 - 0x60: I/O-Buffer
 - Ox64: Status

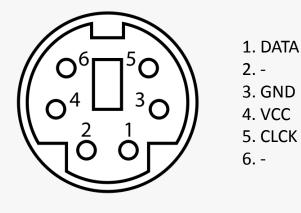


- Communication KBC <-> Keyboard
 - Obvious
 - Scancodes
 - Not that obvious ;)
 - Set LEDs
 - Choose scancode
 - Set repeate rate
 - Keyboard self-test / reset
 - Ping
 - ...

Example (Ping)

KBC sends "ping" (0xEE) via 0x60
KB sends "pong" (0xEE) to 0x60

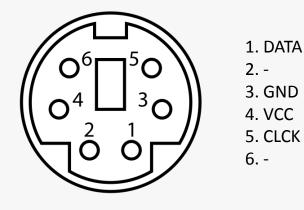
- PS/2 is a serial interface
- Communication
 - DATA
 - ► CLK
 - Bidirectional
 - Keyboard defines clock (30 50 ns)

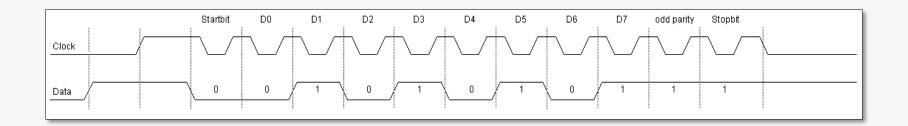


Data frames

- KB (11 bit): startbit, D0-D7 [data], odd parity, stopbit
- KBC (12 bit): startbit, D0-D7 [data], odd parity, stopbit, ACK (KB)

- PS/2 is a serial interface
- Communication
 - DATA
 - ► CLK
 - Bidirectional
 - ▶ Keyboard defines clock (30 50 ns)



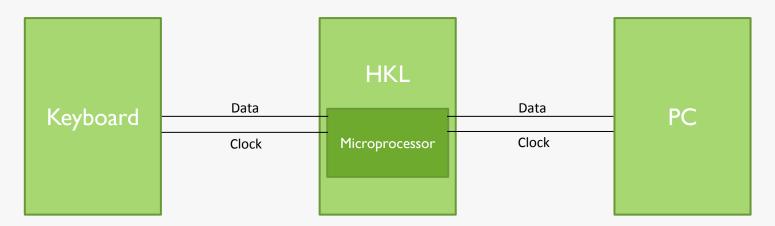


Current measurement

- Additional electronic components
- = Additional power consumption ;)
 - KeyDemon = 65 mA
 - KeyKatcher = 54 mA
- More current is drawn
- Cannot be measured by software

- Keylogger are password protected
 - Entered via Keyboard
 - Ghost typing
 - Shipped with default password
 - Password restore is complex
- Brute Force password
 - Via software
 - Check ghost typing

- Problem
 - Tested HKL don't tap the data line
 - ▶ HKL are placed "inline"



- HKL knows the data flow
- KBC can't send fake keystrokes

However

- Some KB commands (0x60) lead to fake key presses
- Maybe keyboard response is interpreted...

Brute Force password

- Translation Table (KB command -> key press)
- Brute Force attack via Software

Practical?

- Limited amount of chars (~10)
- Not all passwords can be Brute Forced
- Works for: KeyGhost, KEYKatcher (some)

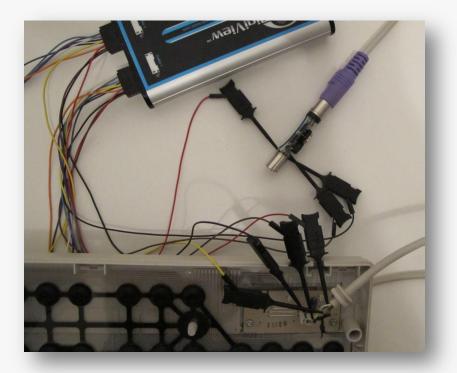
Demo

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- Changes on the line
 - HKL are placed "inline"
- HKL might change signals on the line
 - Different signals (data)
 - Own clock (30-50 ns)
 - Slight dislocation of data/clock signal
 - Maybe more...;)

- Analyze the data flow
 - Tap signal at the keyboard
 - Tap signal after the keylogger



Result:

Jo Digi¥iew - F:\temp\kb.dvdat		
File Config Search Window Help		
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:3 :4 :5 :6		5 00 5 00 5 00 5 00 5 00
:7 :8 :9 Configuration: DV3100 (100 Mhz /18 Channels)	Hardware Buffer:	Son Son Son

- Clock is set to low
 - Delay of the HKL

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File Config Search Window Help				
	•	🜔 🕑 🕑 🕑 🖉 🖉 🖉 🖉 🖉 🖉	No Device	
10,000 ns/Div	<u>₹</u>			
1 Name				
■ - Bus 202) 02)	00	<u> </u>	
:0			5 01	
			§ 01	Keylogger
:2				110 110 00001
:3				
:4			<u>§</u> 00	
:5			<u>\$</u> 00	
:6			<u> </u>	
.7			<u> </u>	
	Delay		5 01	
.9			§ 01 _	Keyboard
Configuration: DV3100 (100 Mhz / 18 Channels)	Hardware Buffer:	0%	AT PFX	

- Clock is set to high
 - Same timing

Digi¥iew - F:\temp\kb.dvdat			_ _ _ _ _
File Config Search Window Help			No Device
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= - Bus 00	X	202	<u> </u>
:0			<u>5</u> 01
			5 01
:2			<u> </u>
:3			<u>\$</u> 00
:4			<u>\$</u> 00
:5			<u> </u>
:6			<u> </u>
.7			<u>\$</u> 00
:8			<u>\$</u> 01
.9			<u>5</u> 01 –
	Useduses Dr. Kan		T PFX
Configuration: DV3100 (100 Mhz / 18 Channels)	Hardware Buffer:	0% P H P A	

- Clock cycles are shorter for HKL
 - Probably HKL generates own clock signal
 - Can be detected on the wire
 - No possibility to detect via software
 - Exact clock state cannot be retrieved by KBC

But the clock signal starts later...

- Remember when clock was pulled low
- HKL might cause a delay on the wire

Time Measurement

- Tested HKL were placed "inline"
- Microprocessor has to analyze the signal and pass it on
- This additional logic increase signal propagation time

DigiView - F:\temp\kb.dvdat	JogiView - F:\temp\kb.dvdat	
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Image Image <thimage< th=""> <thimage< th=""> <thim< th=""><th></th><th></th></thim<></thimage<></thimage<>		
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Data signal (begin)

Data signal (end)

Time Measurement

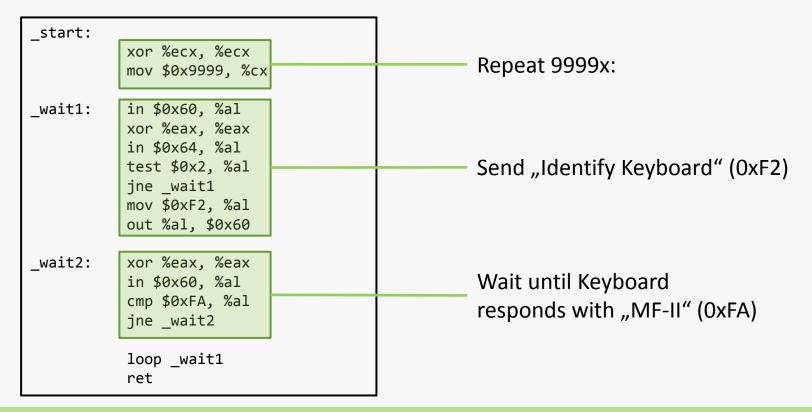
- Tested HKL were placed "inline"
- Microprocessor has to analyze the signal and pass it on
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1 Name 10000 m/Div 1.4761279597500 Mine 1.8464.8 0 0 1 1.4 1 303 203 202 5 30 202 303 5 30 0 1 <			S 🛞 A 🗖 No Device
	1 Name 10.000 nz/Div 1.4761275957500 Mins 1 × Y A B © D 1 2 0 0 0 5 0 303 203 202 5 30 30 203 202 5 0 31 5 0 5 0 32 5 0 5 0 33 5 5 5 5 30 5 5 5 5 30 5 5 5 5 30 5 5 5 5 30 5 5 5 5 30 5 5 5 5 30 5 5 5 5 30 5 5 5 5 30 5 5 5 5 30 5 5 5 5 30 5 5 5 5 30 5 5 5 5 30 5 5<	1 Name 10,000 ns/Div 1,4761364855333 Mins 1 Name 11,1111111111111111111111111111111111	Image: Second

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Basic idea

- Send command to KB, wait for response and measure run time
- Like a "ping"



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- Delay introduced by the HKL is very (!) small
 - Previous code can't be used in "normal OS state"
 - scheduler, interrupts, ...
 - Measurement isn't exact enough
 - Code must run exclusively
 - Get the most accurate measurement

- Solution
 - Loadable Kernel Module
 - Get CPU exclusively
 - Deactivate interrupts for processor
 - Disable kernel preemption
 - SMP locking
 - Run ASM code ("ping")
 - Measure runtime of the code
 - Interrupts are disabled
 - Read processors time stamp counter (rdtsc)
 - Counter is increased every clock cycle
 - Use the number of clock cycles
 - Restore everything and write result to kernel message buffer

- Time Measurement
 - Results

Setup	Clock cycles		
Keyboard	338 1 03523280		
KeyGhost	338 <mark>5</mark> 62656160		
KeyKatcher Mini	338 6 25304965		
KeyKatcher Magnum	338 4 21058298		

- "Inline" HKL can be detected using Time Measurement
 - Measure without HKL
 - Define Baseline (e.g 33820000000)
 - Measure again
 - ► Win ;)

Defeat PS/2 Hardware Keylogger

Fill Keylogger memory via software

- Some stop logging
- Some overwrite memory at the beginning
- Keystrokes are overwritten / not recorded

Keyboard commands

- Some commands lead to fake keypress (see Brute Force)
- Send those repeatedly
- ▶ ~100 logged keys in 10s
- ▶ 109 minutes to fill 64kB

Keyboard command "0xFE"

- Resend
- Keyboard responds by resending the last-sent byte
- ▶ ~4 logged keys in 10 s

Practical?

- Most PS/2 HKL have a few KBytes memory
- Nevertheless takes too much time
- Works for: KeyGhost, KEYKatcher (some)

Defeat PS/2 Hardware Keylogger

Stop HKL from sniffing keystrokes

Keyboard sends scancodes

- Make / Break codes
- Defined in scan code set
- Scan codes set can be choosen via KB command "0xF0"
- 3 scancode sets
 - ▶ 1: XT keyboards
 - > 2: MF2 keyboard
 - ▶ 3: AT keyboads
- Tested Keyloggers support scancode set 2 and 3
- Choose scancode set 1...
 - Keylogger doesn't log correctly
 - Logs can't be used
 - New mapping scancode <-> keycode is necessary for OS
 - hdev
 - ▶ HAL
 - setkeycode

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USB – How does it work

Host controller + Hubs + devices build tree structure

Device has various endpoints

- Buffer in / out
- Configuration via endpoint 0
- ▶ Low Speed devices (Keyboard): endpoint 0 + 2 endpoints with 8 Bytes
- Only host controller manages communication with devices
 - Polls buffer (device configuration)
 - Writes buffer
- Data are transferred as packets

Data transfer types

- Isochronous transfer (guaranteed data rate, no error correction)
- Interrupt transfer (small amount of data, retransmission)
- Bulk transfer (big amount of data, retransmission)
- Control transfer (device configuration, ACKed in both directions)

USB – How does it work

Different device classes

- Plug and Play
- Relevant: HID class
- Defines communication

KB sends 8 Byte input report

- Interrupt Transfer
- Periodically polled by host
- Contains pressed keys
- No make / break codes
- Packet:

Modifier keys	OEM use	Keycode	Keycode	Keycode	Keycode	Keycode	Keycode
Byte 0							Byte 7

USB – How does it work

PC sends 1 Byte output report

- USB Control Transfer
- Control LEDs
- Packet:

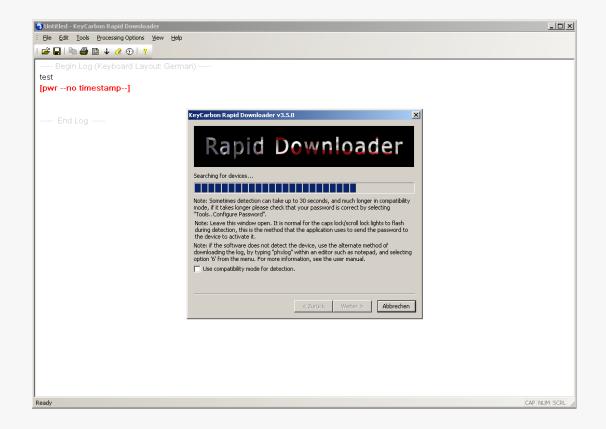
NUM Lock	Caps Lock	Scroll Lock	Compose	KANA	Constant	Constant	Constant
Bit 0							Bit 7

- No additional KB commands
 - Transfer handeld via USB
 - Typematic rate, etc. configured on PC

- Current Measurement
 - Like PS/2
 - More current is drawn
 - Cannot be measured by software
 - Device configuration contains current
 - However no accurate information available

Brute Force KL password

KeyCarbon: software to retrieve keystrokes



Brute Force KL password

- KeyCarbon: software to retrieve keystrokes
- Software needs to communicate with KL...

USB sniffer:

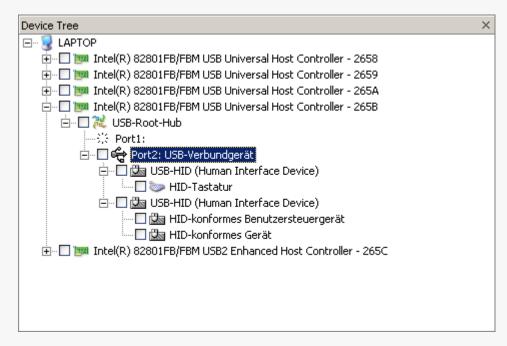
Туре	Seq	Time	Request	Request Details	Raw Data	I/O
START	0001	0:16:31.281				
URB	0002	0:16:44.656	Class Interface	Set Report (Output I	04	out
URB	0003	0:16:44.656	Class Interface	Set Report (Output I	04	out
URB	0004-0003	0:16:44.671	Control Transfer	Set Report (Output I		out
URB	0005-0002	0:16:44.671	Control Transfer	Set Report (Output I		out
URB	0006	0:16:44.812	Class Interface	Set Report (Output I	05	out
URB	0007	0:16:44.812	Class Interface	Set Report (Output I	05	out
URB	0008-0007	0:16:44.812	Control Transfer	Set Report (Output I		out
URB	0009-0006	0:16:44.812	Control Transfer	Set Report (Output I		out
URB	0010	0:16:44.812	Class Interface	Set Report (Output I	03	out
URB	0011	0:16:44.812	Class Interface	Set Report (Output I	03	out
URB	0012-0011	0:16:44.828	Control Transfer	Set Report (Output I		out
URB	0013-0010	0:16:44.828	Control Transfer	Set Report (Output I		out
URB	0014	0:16:44.968	Class Interface	Set Report (Output I	07	out
URB	0015	0:16:44.968	Class Interface	Set Report (Output I	07	out
URB	0016-0015	0:16:44.968	Control Transfer	Set Report (Output I		out
URB	0017-0014	0:16:44.968	Control Transfer	Set Report (Output I		out
URB	0018	0:16:45.109	Class Interface	Set Report (Output I	03	out
URB	0019	0:16:45.109	Class Interface	Set Report (Output I	03	out
URB	0020-0019	0:16:45.109	Control Transfer	Set Report (Output I		out
URB	0021-0018	0:16:45.109	Control Transfer	Set Report (Output I		out
URB	0022	0:16:45.265	Class Interface	Set Report (Output I	07	out
URB	0023	0:16:45.265	Class Interface	Set Report (Output I	07	out

- Software needs to communicate with KL...
 - I Byte output reports (set LEDs)
 - Fixed header + HKL password + footer
 - Password char is encoded with 4 Bytes

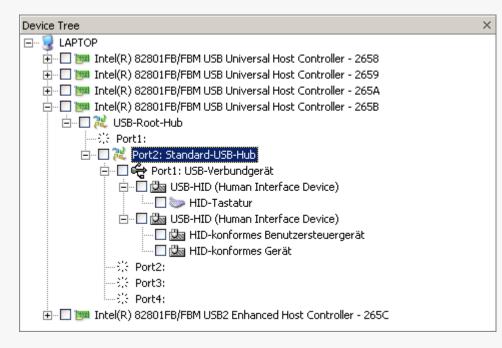
Brute Force (default) passwords

- Create Lookup Table for PW chars
- Perform attack via software
- Works for: KeyCarbon models

- Changes to USB Properties / Topology
 - Keyboard only:



- Changes to USB Properties / Topology
 - Keyboard + KeyCarbon:



- Changes to USB Properties / Topology
 - Addtional USB HUB if KeyCarbon is present

"Why is the device undetectable, in practice, by software? The device shows up in Windows 'Device Manager' as a generic USB hub. This generic USB hub has no ID strings, and is indistinguishable from the generic USB hub found in 90% of all USB hubs. "

► Well...

Device Descriptor						
	Offset	Field	Size	Value	Description	
	0	bLength	1	12h		
L	1	bDescriptorType	1	01h	Device	
L	2	bodUSB	2	0110h	USB Spec 1.1	
L	4	bDeviceClass	1	09h	Hub	
L	5	bDeviceSubClass	1	00h		
L	6	bDeviceProtocol	1	00h		
L	7	bMaxPacketSize0	1	08h	8 bytes	
L	8	idVendor	2	0451h	Texas Instruments, Inc.	
L	10	idProduct	2	2046h		
L	12	bodDevice	2	0125h	1.25	
L	14	iManufacturer	1	00h		
	15	iProduct	1	00h		
	16	iSerialNumber	1	00h		
	17	bNumConfigurations	1	01h		

USB HUB Controller used: Texas Instruments (TUSB2046B)

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- Changes to USB Properties / Topology
 - KeyGhost changes device properties
 - USB Speed
 - □ Keyboard: *bMaxPacketSize0* 08 / Speed: Low
 - □ KeyGhost: *bMaxPacketSize0* 64 / Speed: Full
 - Device Status
 - □ Keyboard : Bus Powered (0x0000)
 - □ KeyGhost : Self Powered (0x0001)
 - More details later...

- Time Measurement
 - Like PS/2
 - HKL are placed inline -> introduces a delay

File Config Search Window Help Image: Search Image: Search Image: Search Image: Search Image: Search Image: Search Image: Search Image: Search Image: Search Image: Search Image: Search Image: Search Image: Search Image: Search Image: Search Image: Search Image: Search Image: Search Image: Search Image: Search	
Image: 200,000 ns/Div ▼18,004 1 Name Image: 1 Image: 1	41600 ms
$= Bus 00 \times 08 \times 04 \times 02 \times 02 \times 02 \times 02 \times 02 \times 02 \times 02$	
	Keylogger
Image: 12 Image: 12 Image: 13 Image: 12	Keyboard

Time Measurement

- Basically the same idea like for PS/2
- Has to be adjusted for USB

PC can send 1 Byte output report to KB (LED)

- sent as Control-Transfer
- Control-Transfer are ACKed
- ▶ Like PS/2 "ping"
- Can be used for runtime measurement ;)

Implementation

- Send output report to KB
- Wait until ACKed
- Do it various times (10.000)
- Measure runtime

Measurement can be performed from userland

▶ e.g. libusb

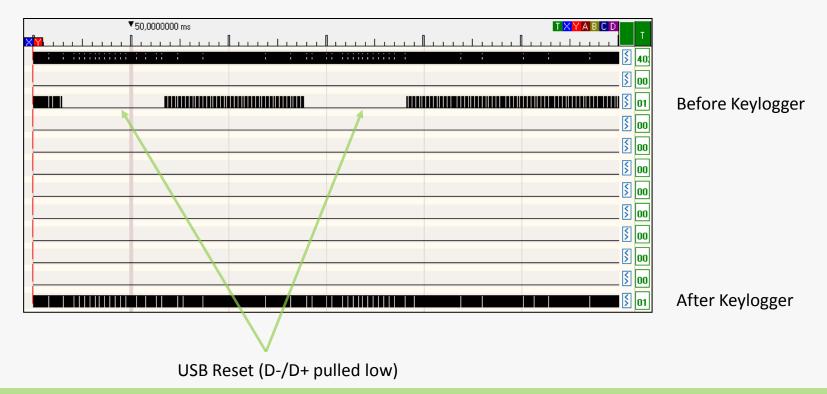
- Time Measurement
 - Results

Setup	Milliseconds
Keyboard	40034
KeyGhost	56331
KeyCarbon	43137

- USB HKL can be detected using Time Measurement
 - Create baseline for default setup (HUBs, etc.)
 - Measure again
 - ► Win ;)

- Different keyboard behaviour
 - Normal behaviour:
 - Interrupt read (8 Byte): \x81\x06\x00\x22\x00\x00\x00\x00\x04
 - Send USB Reset
 - KeyGhost behaviour:
 - Interrupt read (8 Byte): \x81\x06\x00\x22\x00\x00\x00\x00\x04
 - Send USB Reset
 - Interrupt read (8 Byte): \x81\x06\x00\x22\x00\x00\x00\x04

- Different keyboard behaviour
 - Analysis on the wire...
 - Reason: keyboard never receives USB Reset



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- Keyboard never receives USB Reset
- USB single-chip host and device controller (ISP1161A1BD)
 - Acts as Device for PC (causes changes to device properties)
 - Acts as Host Controller for KB
- Behaviour can be tested via software
 - e.g. libusb
- Note: Time Measurement for this design bug is possible too

Conclusion

PS/2

- All tested models were placed "inline"
- Time Measurement as general technique to detect them
- Scancode 1 as general technique to defeat them

USB

- Detection via USB behaviour (USB speed, etc.)
- Individual bugs
- More research to come...
- All tested HKL contained bugs that can be used to detect them
 - Generic and individual bugs
 - Each HKL has to be analyzed seperately
 - Bugs can be combined (Pattern)
- PoC code
 - Soon: https://code.google.com/p/hkd/

Thank you for your interest!

Questions and Feedback

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