



# Virtual Machine Protection Technology and AV industry

Zhenxiang Jim Wang

Microsoft Malware Protection Center



# Intro

- Joined Microsoft in 2007
- Main work in Microsoft:
  - Static unpacker development.
    - ❖ Finished more than 10 static unpackers, including: Molebox, PECompact, PESpin, SVKP, ASProtect, etc
  - Virtual machine technology analysis/research
- MMPC
  - Microsoft Malware Protection Center (MMPC) established in 2006.
  - Partner with other MS security teams (MSRC, WLSP/SmartScreen, etc.)
  - Responsible for protecting users from malicious threats.
  - Provide core Antimalware technology to Microsoft Security Essentials™、 Microsoft® Windows® Defender、 Malicious Software Removal Tool, and Forefront™ products.



# Pervasive Virtualized Packers Affect AV industry

- Agenda
  - Introduction
  - The Inherent Ability of VM to Defeat Emulation
    - ❖ Case Study
  - VM to Dominate Packers
  - The Pervasive Virtualized Packers Defeats Static Unpacking
  - Countermeasure
  - False Positive
  - Bonus Slides – Case Study: Asprotect Stolen Code & Its VM



# Introduction

## Packer Generations & VM Protection Technology

- **Introduction**
- The Inherent Ability of VM to Defeat Emulation
  - Case Study
- VM To Dominate Packers
- Pervasive VM Defeats Static Unpacking
- Countermeasure
- False Positive
- Bonus Slides – Case Study: Asprotect stolen code & its VM



# Introduction: Packers and Generations

- ❑ **Compressor** UPX, ASPack
  
- ❑ **Protector** Asprotect, SVK Protector
  
- ❑ **VM Protection system or virtualised packers**  
Themida, VMProtect.
  - ❖ Need to clarify, ASProtect should be considered as a virtualized packer rather than Protector, because there are 4 VMs used in it.



# Introduction:

## Characteristics and Usage of VM in Packers

- Virtualization is not new technology
  - Used in different fields to virtualise resource, CPU and application, etc.
- In packers, virtualization is used to defeat reverse engineering.
  - ❖ Subverts the concept of traditional packers
  - ❖ Original instructions are converted to VM instructions and removed permanently
  - ❖ VM instructions are interpreted to execute
- Virtualization techniques in packers can be used to protect:
  - Critical function/code snippet
  - Specific instructions, often used in specific situations.  
For example, in Asprotect, two VMs are used to protect special instructions, such as JCC, JMP, CALL etc, in advanced import protection and stolen code
    - See also: Bonus slide about Asprotect stolen code.



# Introduction: VM Implementation

## ❑ The following components are necessary to implement a VM

### ➤ **VM API**

- ❖ Used to enter/exit VM. Usually, you cannot expect to find a CALL instruction☺
- ❖ The code to enter/exit VM can be generated at packing time(Themida, VMProtect, ASProtect) or at runtime time (ASProtect)

### ➤ **VM Context**

- ❖ Contains all info to emulate instructions, such as: (1)VM EIP; (2)The buffer to exchange register values between VM and real CPU; (3)VM handlers info; and (4)other specific info.

### ➤ **VM Handler**

- ❖ VM handlers are used to decode and execute VM instructions



# Introduction: Obfuscation, the Foundation

- ❑ To analyze a VM
  - Understand how VM handlers work and determine the functions of all VM handlers
  - Collect the detailed information about each VM handler
  
- ❑ VM handlers play a critical role in the process of protecting VM from reverse engineering
  - If VM handlers are not safe, the VM is not safe and the applications protected with it will be unsafe
  
- ❑ Obfuscation techniques make the handlers powerful
  - VM handler is usually small and the instructions are straightforward, but obfuscation will make it larger and difficult to understand





# Unpacking: Status Quo

- ❑ How to deal with packed samples is one of the most challenging problems AV industry faces.
  - Packers protect more than 80% of all existing malware.
  
- ❑ The techniques to deal with packers
  - Generic unpacking
    - Traditional emulator and DT. Hereinafter called *emulator*
    - Slow
    - Generic
  
  - Static unpacking
    - Specific
    - Fast
    - Long development time
  
  - The hybrid approach



# VM Defeats Generic Unpacking : The Inherent Ability of VM to Defeat Emulation

- Introduction
- **The Inherent Ability of VM to Defeat Emulation**
  - Case Study
- VM To Dominate Packers
- Pervasive VM Defeats Static Unpacking
- Countermeasure
- False Positive
- Bonus Slides – Case Study: Asprotect stolen code & its VM



## VM Defeats Generic Unpacking

- ❑ The emulators suffer resource exhaustion when trying to run through virtualized packers.
- ❑ Time to emulate a sample packed by a virtualized packer is often too long to tolerate, especially for on-access scan.



# Case study: Themida VM Implementation

- Introduction
- The Inherent Ability of VM to Defeat Emulation
  - **Case Study**
- VM To Dominate Packers
- Pervasive VM defeats Static Unpacking
- Countermeasure
- False Positive
- Bonus Slides – Case Study: Asprotect stolen code & its VM



# Case Study: Themida - Patterns

- ❑ Patterns are widely used in virtualized / obfuscated packers, including Themida.
  
- ❑ What's a pattern?
  - An Instruction snippet
  - Used repeatedly
  - Makes analysis hard
  - Equivalent to a shorter instruction snippet



# Case Study: Themida

## Types of Patterns

### ❑ Junk Pattern

- › Does nothing and can be removed safely

### ❑ Instruction-level pattern


- › Is equivalent to a single instruction
- › Can be replaced by its equivalent instruction

### ❑ Function-Level Pattern

- › Equivalent to a shorter instruction snippet

### Example: Function-level pattern

```
Pushf  
shr  dword ptr [esp], 6  
not  dword ptr [esp]  
and  dword ptr [esp], 1  
push eax  
push edx  
mov  eax, 12DCB261h  
add/sub  eax, 0ED234D44h  
mul  dword ptr [esp+8]  
lea  eax, [eax+ebp+403767h]  
mov  [esp+8], eax  
pop  edx  
pop  eax  
lea  esp, [esp+4]  
jmp  dword ptr [esp-4]
```



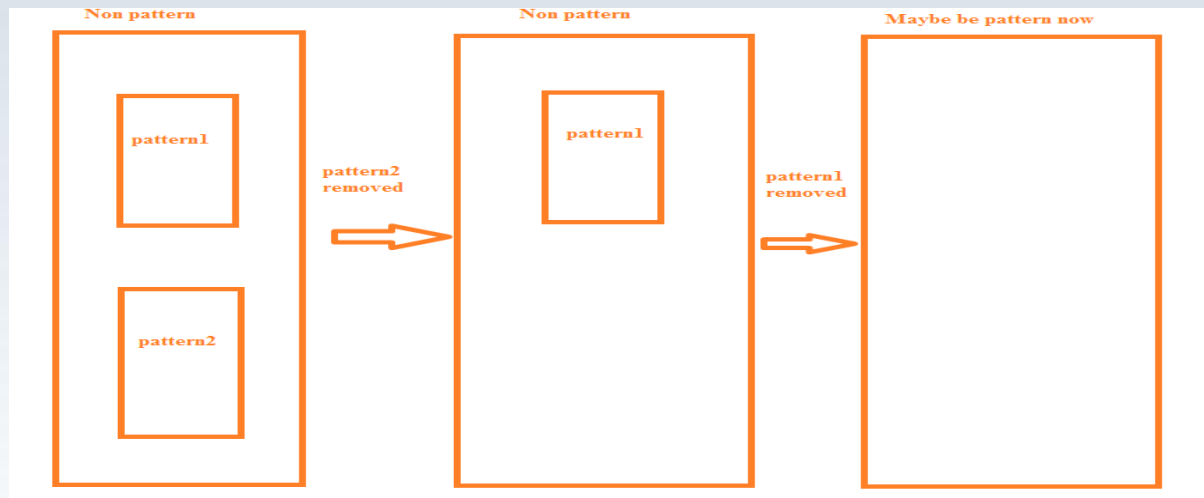
```
dec  ecx  
jnz  xxxx  
jmp  yyyy
```



# Case Study: Themida

## Rule to Define Patterns

- ❑ **Rule1:** The instruction snippet should be equivalent to a shorter one
- ❑ **Rule2:** The instruction snippet should not contain any instruction snippet that can be defined as another pattern. The principle can be named as **MINIMAL** principle.





# Case Study: Themida

## Apply Patterns at Packing Time

- Applying patterns to obfuscate VM handlers
  - For each instruction to obfuscate in a handler, an equivalent instruction-level pattern is chosen randomly to replace, and then do the same thing for the new code snippet

Example: Apply patterns on the instruction **PUSH EAX**:

- **Round #1:** Assume choosing the pattern to replace the instruction **PUSH EAX**
  - **PUSH IMM**
  - **MOV [ESP], REG -> PUSH REG**
  - The instruction will be replaced as
    - **PUSH EAX -> PUSH IMM**
    - **MOV [ESP], EAX**
- **Round #2:** Assume choosing the pattern to replace the instruction **PUSH IMM**
  - **SUB ESP, 4**
  - **MOV [ESP], IMM -> PUSH IMM**
  - The instruction snippet will be extended to:
    - **PUSH EAX -> PUSH IMM -> SUB ESP, 4**
    - **MOV [ESP], EAX MOV [ESP], IMM**
    - **MOV [ESP], EAX**
- **Round #3:** The instruction **SUB ESP, 4** will be replaced by a randomly chosen pattern, and so on.



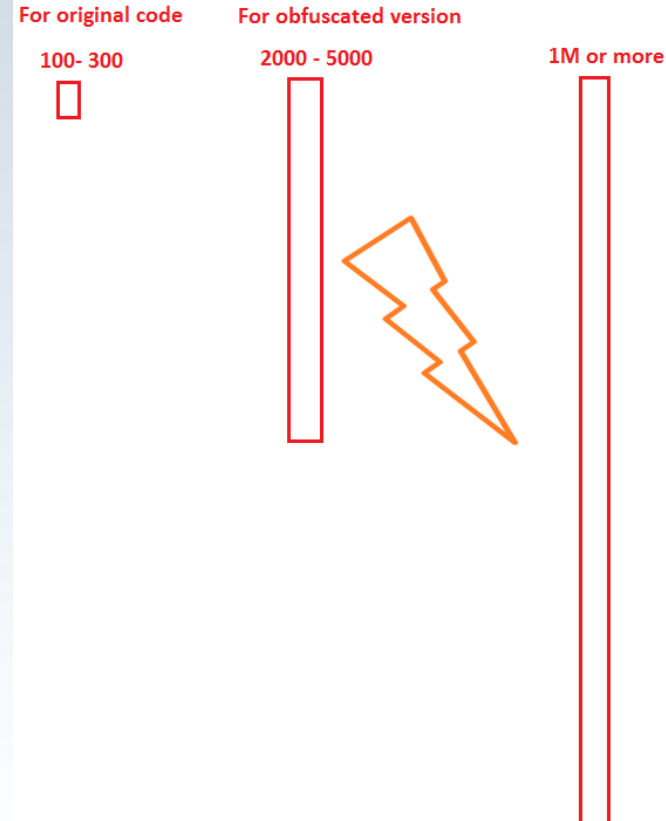


# Case Study: Themida

## The Ability of Anti-Emulation

- Obviously, the implementation mechanism makes it easy to extend the instruction number of a handler to **1M or more**. This will defeat generic unpacking easily

The number of instructions in a Themida VM handler





# VM Tips the Balance

- In the early days, ***signature-based*** approach was used to detect viruses
- Malware authors adopted the ***polymorphic technique*** to counteract the approach.
- ***Emulation technique*** was used to solve the polymorphism issue.
- Malware authors adopt ***virtualization technique*** to defeat emulation.
- Virtualization technique tips the balance of power toward malware authors. ***What is the next story?***



# VM to Dominate Packers

- Introduction
- The Inherent Ability of VM to Defeat Emulation
  - Case Study
- **VM to Dominate Packers**
- Pervasive VM defeats Static Unpacking
- Countermeasure
- False Positive
- Bonus Slides – Case Study: Asprotect stolen code & its VM



## VM to Dominate Packers

- ❑ Virtualized packers do not occupy a dominant position currently in packer distribution.
- ❑ There is an upward trend in the prevalence of virtualized packers in packer distribution.
- ❑ Virtualization is becoming a must-have for new developed packers, existing packers are adding the virtualization function.



# VM to Dominate Packers

- ❑ What if the open-source packer, UPX, the most popular, statistically, adopts VM techniques
- ❑ Open-source VM engine
- ❑ VM generator
  - Users just need to define syntax of VM instructions.
- ❑ It can be predicted reasonably that more and more malware authors will adopt virtualized packers, either existing virtualized packers or custom virtualized packers written by the malware authors themselves, in order to protect their “works” in the near future.



# Pervasive VM Defeats Static Unpacking

- Introduction
- The Inherent ability to defeat emulation
  - Case Study
- VM To Dominate Packers
- **Pervasive VM Defeats Static Unpacking**
- Countermeasure
- False Positive
- Bonus Slides – Case Study: Asprotect stolen code & its VM



# What About Static Unpacking

- ❑ Static unpacking development focus on the packers that
  - Cannot be emulated
  - Takes a long time to emulate
  - Significant performance improvement because of prevalence
  
- ❑ It is still feasible to develop a static unpacker for limited number of prevalent packers, but ...
  
- ❑ We may not have enough resources to analyze and optimize numerous unknown virtualized packers even with the help of de-obfuscation tools.  
The prevalence of custom virtualized packers will make static unpacking techniques unfeasible.
  - For example, it took several months to implement Asprotect static unpacker because there are more than 160 versions. Asprotect has a long history. But for custom packers, you will find 160 versions in a shorter period.



# Countermeasure

Strategic improvement  
Technical improvement

- Introduction
- The Inherent Ability of VM to Defeat Emulation
  - Case Study
- VM To Dominate Packers
- Pervasive VM defeat Static Unpacking
- **Countermeasure**
- False Positive
- Bonus Slides – Case Study: Asprotect stolen code & its VM





# Strategic - Change the Ecosystem

- ❑ AV is passive now
- ❑ Collaborate with commercial packer vendors
- ❑ Get help from the published application vendors
  - If they adopt VM/obfuscation techniques in their applications



# Strategic - Commercial Packers

- ❑ Blacklist all samples packed with unlicensed commercial packers(Shareware)
- ❑ Blacklist licensed packers used in malware
- ❑ Blacklist all samples packed with pirated commercial packers.
  - Currently, some AV vendors collect the licensed info of samples to determine if they are packed by a pirated packer *in their own way*. We need a more **robust, consistent** mechanism to identify the pirated packers.



# Strategic - Commercial Packers

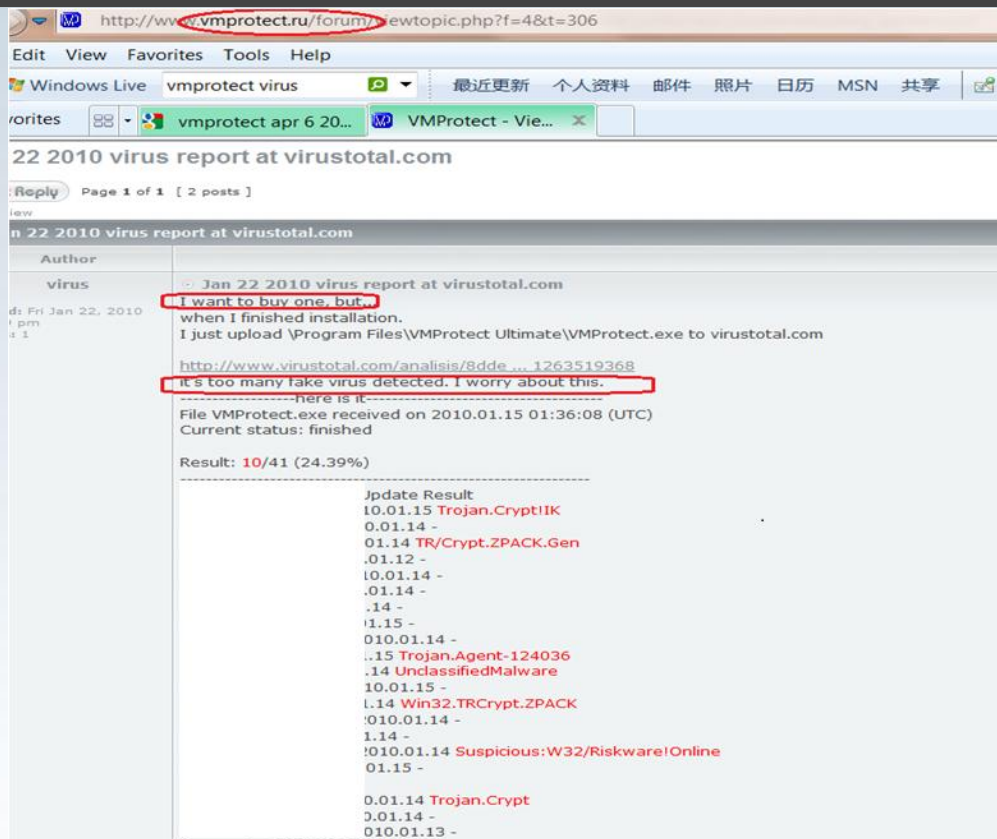
```
00000000 74 65 63 74 20 4C 69 63+ db 0Dh,0Ah
00000000 65 6E 73 65 20 44 75 6D+ db '[YouProductName], [1.0], ',0Dh,0Ah
00000000 70 0D 0A 0D 0A 5B 59 6F+ db '[1.4 build 01.26 Beta], [Teddy Rogers]',0Dh,0Ah
00000000 75 50 72 6F 64 75 63 74+seg000 ends
00000000 4E 61 6D 65 5D 2C 20 5B+
00000000 31 2E 30 5D 2C 20 0D 0A+
```

Teddy Rogers is the site administrator of [www.tuts4you.com](http://www.tuts4you.com)

```
65 6E 73 65 20 44 75 6D+ db '[ApplicationName], [Version], ',0Dh,0Ah
70 0D 0A 0D 0A 5B 41 70+ db '[2.3 build 04 26 Beta], [stephenteh',0Dh,0Ah
70 6C 69 63 61 74 69 6F+ db 'TEAM RESURRECTION]--',0Dh,0Ah
6E 4E 61 6D 65 5D 2C 20+seg000 ends
5B 56 65 72 73 69 6F 6E+
```



# Strategic - Commercial Packers



Packer vendors should have motivation to provide more help ☺



# Strategic – Handling VM Apps

- ❑ Report to White List Association
- ❑ Digitally sign their applications



# Technical – Invest in Unpacking

- Most prevalent virtualized commercial packer
- ▶ It is worth investing in
  - Developing static unpacker
    - ❖ Asprotect static unpacking: including restoring virtualized x86 instructions, recovering stolen OEP, stolen functions, missing Delphi init/term table etc, the unpacked file can run normally
      - See also: Bonus slide: Case Study: Asprotect stolen code & its VM
  - The hybrid approach of generic unpacking and static unpacking. Implement VM statically on the basis of emulation.
    - ❖ Themida: recover virtualized x86 instructions
- Numerous unknown virtualized custom packers.
  - Generic unpacking, static unpacking and the hybrid will fail.



## Technical – Deal with Unknown

- ❑ If emulator cannot run through, maybe we can adopt the combination of full-fledged emulation technique and behavior analysis.
  - Full-fledged emulator will defeat the anti-emulation and virtualized code
  - APIs will just use to record behaviors.
  - This should be an additional component.



# False Positive

- Introduction
- The Inherent Ability of VM to Defeat Emulation
  - Case Study
- VM To Dominate Packers
- Pervasive VM defeats Static Unpacking
- Countermeasure
- **False Positive**
- Bonus Slides – Case Study: Asprotect stolen code & its VM





# An interesting Note on PECompact

PECompact Executable Compressor - Windows Internet Explorer

http://www.bitsum.com/pecompact.php

File Edit View Favorites Tools Help

Windows Live pecompact 最近更新 个人资料 邮件 照片 日历 MSN 共享

PECompact Exec... 众多是什么意思\_翻... 众多的英文翻译, ...

LZMA - FFCE - aPLib - JCALG1 - BriefLZ

**Other Codec Plug-ins:**  
Password Protect - MessageBox - Invert - Copy - Expand

**API Hook Plug-ins:**  
Fast-Import - Redirect

**Loader Plug-ins:**  
Anti-Debug - Debug - Enhanced Anti debug - Reduced

navors. Our test cases assure proper functioning.

**Anti-Virus Interoperability:**

- PECompact has a low false alarm rate in comparison to other executable compressors.
- Viruses can not hide within compressed modules because

## It implies at least two things:

- ❖ There are a few false positives
- ❖ There are false positives, even for compressors



## False Positive

- ❑ Even now, we can find many false positives.
  - These false positives may be due to packer blacklisting. Some in the industry may argue that the benefits for protection overweigh the harm caused by FPs. Users may disagree.
  
- ❑ Industry likely continue to see false positives of this sort in the future.



# False Positive

- It will be much more difficult to avoid false positive completely when adopting behavior analysis techniques

```
13.0.900 2010.04.28 -
y 7.0.0.125 2010.04.28 -
5.400.0.1158 2010.04.28 -
W- 6.8.5 2010.04.28 Heuristic.LooksLike.Win32.Suspicious.H
c 1.5703 2010.04.28 -
5067 2010.04.28 -
6.04.11 2010.04.28 -
sigcheck: publisher....: Microsoft Corporation
copyright....: (c) Microsoft Corporation. All rights reserved.
product.....: Microsoft_Windows_Operating System
description..: Windows Calculator application file
original name: CALC.EXE
internal name: CALC
file version.: 5.1.2600.0 (xpclient.010817-1148)
comments.....: n/a
signer.....:
signing date.: -
6231 2010.04.28 -
20091.2.0.41 2010.04.28 -
```



# A Word on Cloud Computing

- ❑ Many Web-based application/platform available
- ❑ Security issues continue to concern people, because they will lose control of their information in the cloud computing environment.
- ❑ But cloud computing might be a way to defeat rampant virtualized viruses on the desktop.



## Conclusion

- ❑ With the prevalence of virtual machine protection techniques, AV industry might be at a turning point
- ❑ We may need to take a more active strategy
- ❑ We need new techniques to deal with virtualized packers, just like adopting emulation technique to deal with polymorphic viruses.



Thank You

[jimwan@microsoft.com](mailto:jimwan@microsoft.com)



# Bonus Slides: Case study: ASProtect stolen code & its VM

- Introduction
- The Inherent Ability of VM to Defeat Emulation
  - Case Study
- VM To Dominate Packers
- Pervasive VM defeats Static Unpacking
- Countermeasure
- False Positive
- **Bonus Slides – Case Study: Asprotect stolen code & its VM**



# Virtual Machines in ASProtect

- ❑ There are four VMs in Asprotect.
  - Two of them are used to protect critical functions
  - One is used to protect stolen code
  - One is used to protect advanced import protection(AIP)
  
- ❑ Two completely different implementations
  - Soft CPU to protect critical functions
  - Standard VM to protect ***stolen code*** & Advanced import protection(AIP)





# What's Stolen code

- ❑ The original code snippet is placed somewhere else in the file or a dynamically allocated memory
- ❑ A JMP instruction to the stolen code is inserted at the beginning of the original code snippet
- ❑ The stolen code is often protected using obfuscation technology
- ❑ **Stolen OEP(Original entry point) is a special case**
  - ▶ The address of stolen OEP is often computed dynamically



## Asprotect Steals Many Code in Different Ways

- ❑ ***Missing functions.*** Some functions are replaced by equivalent obfuscated code snippets
- ❑ ***The function to process the init table in Delphi applications*** is replaced by an obfuscated code snippet and the init table is destroyed.
- ❑ ***The OEP and the licensed functions*** are stolen in a much more complicated way.



# How ASProtect Steals OEP code

- Six steps:
  - Scan the OEP code and generate new basic blocks for CALL, JMP & JCC instructions
  - Obfuscate the OEP code snippet
    - Use many different de-optimization techniques, such as def-use chain, const expand, junk patterns, etc.
  - Divides the obfuscated code snippet into different block randomly
  - Virtualize some special instructions, such as JCC/JMP, CMP, etc
  - Encrypt the return address of the CALL instructions inside the code snippet
  - Encrypt the obfuscated stolen OEP code



# How to Recover Stolen OEP

- The reverse process to recovering the equivalent OEP code snippet is as follows:
  - Decrypt the obfuscated code snippet
  - Recover virtual machine emulated instructions, including CALL instructions
  - Generate correct return address for the emulated CALL instructions
  - De-obfuscate the code snippet
    - ❖ Scan the code snippet and generate the intermediate representation for each instruction
    - ❖ De-obfuscate based on the IR format instructions
    - ❖ Generate opcode for de-obfuscated instructions, in IR format
  - Compute target addresses of CALL/JCC/JMP instructions
  - Generate opcodes for all de-obfuscated instructions



# An Example

The original entry point of ATTRIB.EXE in XP

```
-----  
text:01002200  
text:01002200 6A 28  
text:01002200 68 68 12 00 01  
text:01002200 E8 90 01 00 00  
text:01002204 33 FF  
text:01002206 57  
text:01002207 FF 15 08 10 00 01  
text:0100220D 66 81 38 4D 5A  
text:010022C2 75 1F  
text:010022C4 8B 48 3C  
text:010022C7 03 C8  
text:010022C9 81 39 50 45 00 00  
text:010022CF 75 12  
text:010022D1 0F B7 41 18  
text:010022D5 3D 0B 01 00 00  
text:010022D8 74 1F  
text:010022DC 3D 0B 02 00 00  
text:010022E1 74 05  
text:010022E3  
text:010022E3  
text:010022E3 89 7D E4  
text:010022E6 EB 27  
text:010022E8  
text:010022E8  
text:010022E8 83 B9 84 00 00 00 0E  
text:010022EF 76 F2  
text:010022F1 33 C0  
text:010022F3 39 B9 F8 00 00 00  
text:010022F9 EB 0E  
text:010022FB  
text:010022FB  
text:010022FB 83 79 74 0E  
text:010022FF 76 E2  
text:01002301 33 C0  
text:01002303 39 B9 E8 00 00 00  
text:01002309  
text:01002309  
text:01002309 0F 95 C0  
text:0100230C 89 45 E4  
text:0100230F  
-----  
loc_10022E3: ; CODE XREF: start+1A1j  
; start+271j ...  
nov [ebp+var_1C], edi  
jnp short loc_100230F  
-----  
loc_10022E8: ; CODE XREF: start+391j  
cnp dword ptr [ecx+84h], 0Eh  
jbe short loc_10022E3  
xor eax, eax  
cnp [ecx+0F8h], edi  
jnp short loc_1002309  
-----  
loc_10022FB: ; CODE XREF: start+321j  
cnp dword ptr [ecx+74h], 0Eh  
jbe short loc_10022E3  
xor eax, eax  
cnp [ecx+0E8h], edi  
-----  
loc_1002309: ; CODE XREF: start+511j  
setnz al  
nov [ebp+var_1C], eax
```



# Decrypt Routine

- The routine to decrypt the stolen OEP

```
debug133:02AF0000      :org 2AF0000h
debug133:02AF0000      assume es:debug010, ss:debug010, ds:debug010, fs:debug010, gs:debug010
debug133:02AF0000 66 8B CF      mov     cx, di
debug133:02AF0003 E8 0E 00 00 00 call   sub_2AF0016
debug133:02AF0008 AE          dec     esi
debug133:02AF0009 6F          outsd
debug133:02AF000A 7C 05      jl     short near ptr loc_2AF0010+1
debug133:02AF000C 5A          pop     edx
debug133:02AF000D 8B 68 81    mov     ebp, [eax-7Fh]
debug133:02AF0010      loc_2AF0010:                ; CODE XREF: debug133:02AF0000tj
debug133:02AF0010      db     26h, 67h
debug133:02AF0010      adc     al, 000h
debug133:02AF0014 02 03      mov     dl, 3
debug133:02AF0016      ; ----- SUBROUTINE -----
debug133:02AF0016      sub_2AF0016 proc near      ; CODE XREF: debug133:02AF0003Tp
debug133:02AF0016      call   sub_2AF002E
debug133:02AF0016      pop     edi
debug133:02AF0016      lodsb
debug133:02AF0016      jnz     short near ptr loc_2AF0025+4
debug133:02AF0016      jnp     short near ptr 00000000
debug133:02AF0016      icebp
debug133:02AF0016      setalc
debug133:02AF0016      push  edi
debug133:02AF0016      inc     esp
debug133:02AF0016      loc_2AF0025:                ; CODE XREF: sub_2AF0016+71j
debug133:02AF0016      sub     eax, 2900F362h
debug133:02AF0016      scasb
debug133:02AF0016      dec     edi
debug133:02AF0016      fsubr  st(5), st
debug133:02AF0016      sub_2AF0016 endp         ; sp=00000000, FPU=0
debug133:02AF0016      ; ----- SUBROUTINE -----
debug133:02AF0016      sub_2AF002E proc near      ; CODE XREF: sub_2AF0016Tp
```





# Emulate Special Instructions

```
debug133:02AF036E          loc_2AF036E:                ; CODE XREF: debug133:02AF036A↑j
debug133:02AF036E  C7 01 68 12 00 01          mov     dword ptr [ecx], offset unk_1001268
debug133:02AF0374  59                          pop     ecx
debug133:02AF0375  8F 01                       pop     dword ptr [ecx]
debug133:02AF0377  59                          pop     ecx
debug133:02AF0378  66 9D                       popfd
debug133:02AF037A  68 C7 62 52 3A             push   3A5262C7h
debug133:02AF037F  E8 B8 CE DF AE             call   near ptr 00100230h
debug133:02AF0384  57                          push   edi
debug133:02AF0385  E9 FF 03 00 00             jmp    loc_2AF0789
debug133:02AF038A  FF                          ;
debug133:02AF038B  75                          db  0FFh
debug133:02AF038C  04                          db  75h  ; u
debug133:02AF038D  68                          db  004h ; +
debug133:02AF038E  0B                          db  68h  ; h
debug133:02AF038F  0B                          db  0Bh  ; ii
```

Virtual machine is used to emulate a CALL instruction

The virtual machine technique is used to emulate some special instructions





# Recovered OEP code

```
seg001:010022A8
seg001:010022A8
seg001:010022A8
seg001:010022A8 E7 53 3D 00 00
seg001:010022A8
seg001:010022A8
seg001:010022A8
seg001:010022A8 ED 8A D0
seg001:010022A8 B9 98 A6 A0 E0 B2 A9 29+
.data:01006000
.data:01006000
.data:01006000
.data:01006000
.data:01006000 6A 28
.data:01006000 40 00 12 00 01
.data:01006007 E8 19 02 00 00
.data:01006007
.data:0100600C 33 FF
.data:0100600E 57
.data:0100600F FF 15 08 10 00 01
.data:0100600F
.data:01006015 66 81 38 4D 5A
.data:0100601A 0F 85 28 00 00 00
.data:0100601A
.data:01006020 88 48 3C
.data:01006023 03 C8
.data:01006025 81 39 58 45 00 00
.data:01006028 0F 85 1A 00 00 00
.data:01006028
.data:01006031 0F 87 41 18
.data:01006035 3D 08 01 00 00
.data:0100603A 0F 84 1A 01 00 00
.data:0100603A
.data:01006040 3D 08 02 00 00
.data:01006045 0F 84 EF 00 00 00
.data:01006045
.data:01006048
.data:01006048
.data:01006048
.data:01006048 89 7D E4
.data:01006048
.data:0100604E
.data:0100604E 89 7D FC
.data:01006051 6A 01
.data:01006053 FF 15 14 10 00 01
.data:01006053
.data:01006059 59
.data:0100605A 83 00 18 40 00 01 FF
.data:01006061 83 00 1C 40 00 01 FF
.data:01006068 FF 15 18 10 00 01
.data:01006068

start      public start
proc near
jmp start_0
start      endp

start_0    proc near
; CODE XREF: startj
push      2jh
push      offset duord_1001268
call     sub_1006225
xor      edi, edi
push     edi          ; IpModuleName
call     ds:GetModuleHandleA
cmp      word ptr [eax], 5A4Dh
jnz      loc_1006048
mov      ecx, [eax+3Ch]
add      ecx, eax
cmp      dword ptr [ecx], 4550h
jnz      loc_1006048
movzx   eax, word ptr [ecx+18h]
cmp      eax, 1008h
jz       loc_100615A
cmp      eax, 2008h
jz       loc_100613A

loc_1006048: ; CODE XREF: start_0+1Atj
; start_0+20Tj ...
mov      [ebp-1Ch], edi

loc_100604E: ; CODE XREF: start_0+155Tj
mov      [ebp-4], edi
push    1
call    ds:__set_app_type
pop     ecx
or      ds:dword_1004018, 0FFFFFFFFh
or      ds:dword_100401C, 0FFFFFFFFh
call    ds:_p_Fnode
```



# Comparison

```
text:01002200
text:01002200 6A 28
text:01002200 68 68 12 00 01
text:01002200 E8 90 01 00 00
text:01002204 33 FF
text:01002206 57
text:01002207 FF 15 08 10 00 01
text:01002200 66 81 38 40 5A
text:010022C2 75 1F
text:010022C4 8B 48 3C
text:010022C7 03 C8
text:010022D9 81 39 50 45 00 00
text:010022CF 75 12
text:010022D1 0F 87 41 18
text:010022D5 3D 0B 01 00 00
text:010022D8 74 1F
text:010022DC 3D 0B 02 00 00
text:010022E1 74 05
text:010022E3
text:010022E3
text:010022E3
text:010022E3 89 7D E4
text:010022E6 EB 27
text:010022E8
text:010022E8
text:010022E8 83 89 84 00 00 00 0E
text:010022EF 76 F2
text:010022F1 33 C0
text:010022F3 39 89 F8 00 00 00
text:010022F9 EB BE
text:010022FB
text:010022FB
text:010022FB 83 79 74 0E
text:010022FF 76 E2
text:01002301 33 C0
text:01002303 39 89 E8 00 00 00
text:01002309
text:01002309
text:01002309 0F 95 C0
text:0100230C 89 45 E4
text:0100230F

push 28h
push offset stru_1001268
call _SEH_prolog
xor edi, edi
push edi ; lpModuleName
call ds:GetModuleHandleA
cnp word ptr [eax], 5A0Dh
jnz loc_10022E3
mov ecx, [eax+3Ch]
add ecx, eax
cnp dword ptr [ecx], 4550h
jnz short loc_10022E3
movzx eax, word ptr [ecx+18h]
cnp eax, 10Bh
jz short loc_10022FB
cnp eax, 20Bh
jz short loc_10022E8

loc_10022E3:
; CODE XREF: start+107j
; start+277j ...
mov [ebp+var_1C], edi
jmp short loc_100230F

loc_10022E8:
; CODE XREF: start+397j
cnp dword ptr [ecx+8Ah], 0Eh
jbe short loc_10022E3
xor eax, eax
cnp [ecx+0F8h], edi
jnp short loc_1002309

loc_10022FB:
; CODE XREF: start+327j
cnp dword ptr [ecx+74h], 0Eh
jbe short loc_10022E3
xor eax, eax
cnp [ecx+0E8h], edi

loc_1002309:
; CODE XREF: start+517j
setnz al
mov [ebp+var_1C], eax

.data:01006000
.data:01006000 6A 28
.data:01006000 66 81 38 40 5A
.data:01006007 E8 19 02 00 00
.data:01006008 33 FF
.data:0100600E 57
.data:0100600F FF 15 08 10 00 01
.data:0100600F
.data:01006015 66 81 38 40 5A
.data:0100601A 0F 85 20 00 00 00
.data:0100601A
.data:01006020 80 48 3C
.data:01006023 03 C8
.data:01006025 81 39 50 45 00 00
.data:0100602B 0F 85 10 00 00 00
.data:0100602B
.data:0100602B 0F 87 41 18
.data:01006031 3D 0B 01 00 00
.data:0100603A 0F 84 10 01 00 00
.data:0100603A
.data:0100603A 3D 0B 02 00 00 00
.data:0100603A
.data:01006045 0F 84 EF 00 00 00
.data:01006045
.data:0100604B
.data:0100604B
.data:0100604B 89 7D E4
.data:0100604B
.data:0100604B
.data:0100604E 89 7D FC
.data:01006051 6A 01
.data:01006053 FF 15 14 10 00 01
.data:01006053
.data:01006059 59
.data:01006059
.data:0100605A 83 00 18 A0 00 01 FF
.data:01006061 83 00 1C A0 00 01 FF
.data:01006068 FF 15 18 10 00 01
.data:01006068

proc near ; CODE XREF: start7j
push 28h
push offset dword_1001268
call sub_1006225
xor edi, edi
push edi ; lpModuleName
call ds:GetModuleHandleA
cnp word ptr [eax], 5A0Dh
jnz loc_100604B
mov ecx, [eax+3Ch]
add ecx, eax
cnp dword ptr [ecx], 4550h
jnz loc_100604B
movzx eax, word ptr [ecx+18h]
cnp eax, 10Bh
jz loc_100615A
cnp eax, 20Bh
jz loc_100613A

loc_100604B:
; CODE XREF: start_0+107j
; start_0+207j ...
mov [ebp-1Ch], edi

loc_100604E:
; CODE XREF: start_0+1557j
mov [ebp-4], edi
push 1
call ds:__set_app_type
pop ecx
or ds:dword_1004018, 0FFFFFFFh
or ds:dword_100401C, 0FFFFFFFh
call ds:_p_Freede
```

There are different branches between the original OEP and the recovered OEP



**Microsoft®**  
*Your potential. Our passion.™*

© 2010 Microsoft Corporation. All rights reserved. Microsoft, Windows, Windows Vista and other product names are or may be registered trademarks and/or trademarks in the U.S. and/or other countries. The information herein is for informational purposes only and represents the current view of Microsoft Corporation as of the date of this presentation. Because Microsoft must respond to changing market conditions, it should not be interpreted to be a commitment on the part of Microsoft, and Microsoft cannot guarantee the accuracy of any information provided after the date of this presentation. MICROSOFT MAKES NO WARRANTIES, EXPRESS, IMPLIED OR STATUTORY, AS TO THE INFORMATION IN THIS PRESENTATION.